

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned

Examiner: To Be Assigned

In Re PATENT APPLICATION OF:

Applicants	:	Masaru TAKAISHI)	
)	
Serial No.	:	To Be Assigned)	
)	
Filed	:	January 29, 2004)	INFORMATION
)	DISCLOSURE
)	<u>STATEMENT</u>
For	:	METHOD FOR MANUFACTURING)	
		SEMICONDUCTOR DEVICE)	
)	
Attorney Ref.	:	AI 322)	
)	

January 29, 2004

Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached is a copy of one Japanese publication. The document is listed on the attached Form PTO-1449. Any relevance of this document can be gleaned from Figures 1 to 4.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,



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January 29, 2004
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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket AI 322		Application No. To Be Assigned	
				Applicant Masaru TAKAISHI			
				Filing Date January 29, 2004		Group Unit To Be Assigned	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA						
	AB						
	AC						
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	AE						
	AF						
	AG						
	AH						
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	AJ						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub-Class	Translation
	AK	2003-303785	10/24/03	Japan			NO
	AL						
	AM						
	AN						
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OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
	AP						
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Examiner					Date Considered		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							